

FIG. 1 is a perspective view of the device.

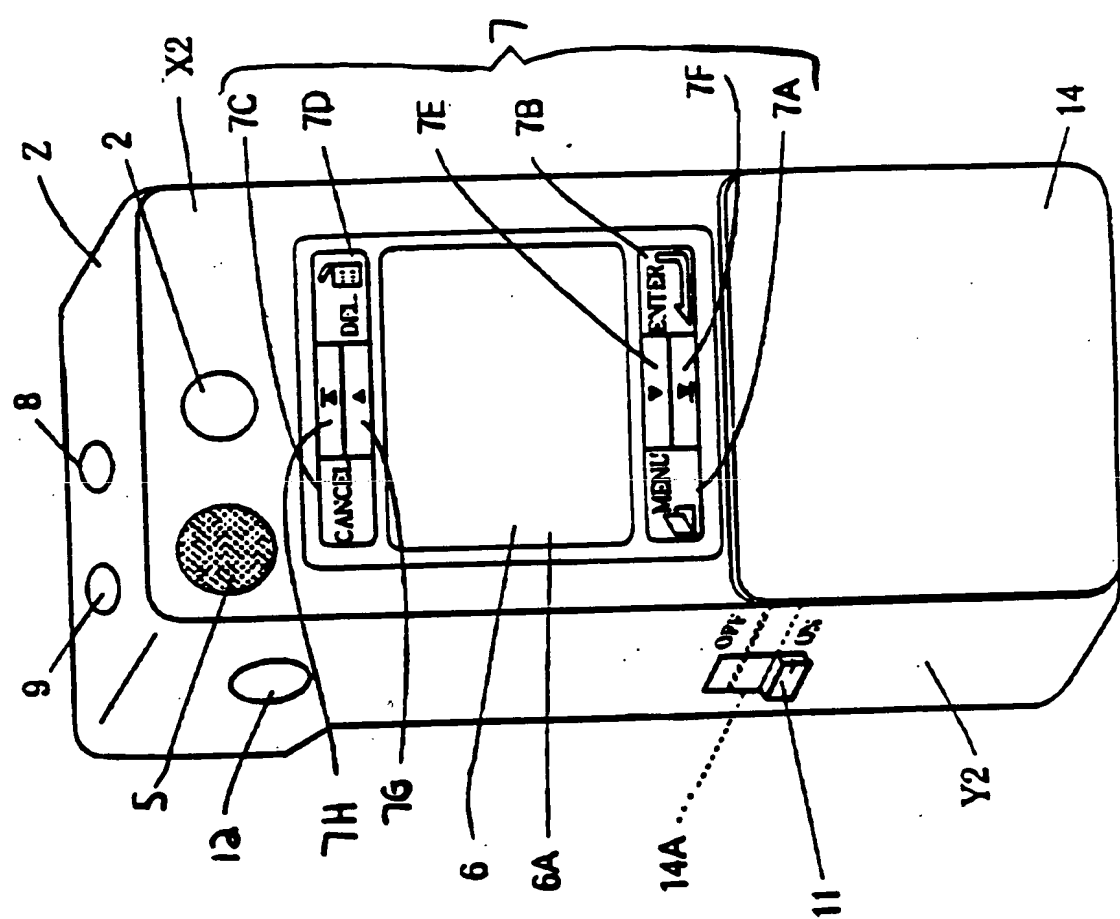


FIG. 2

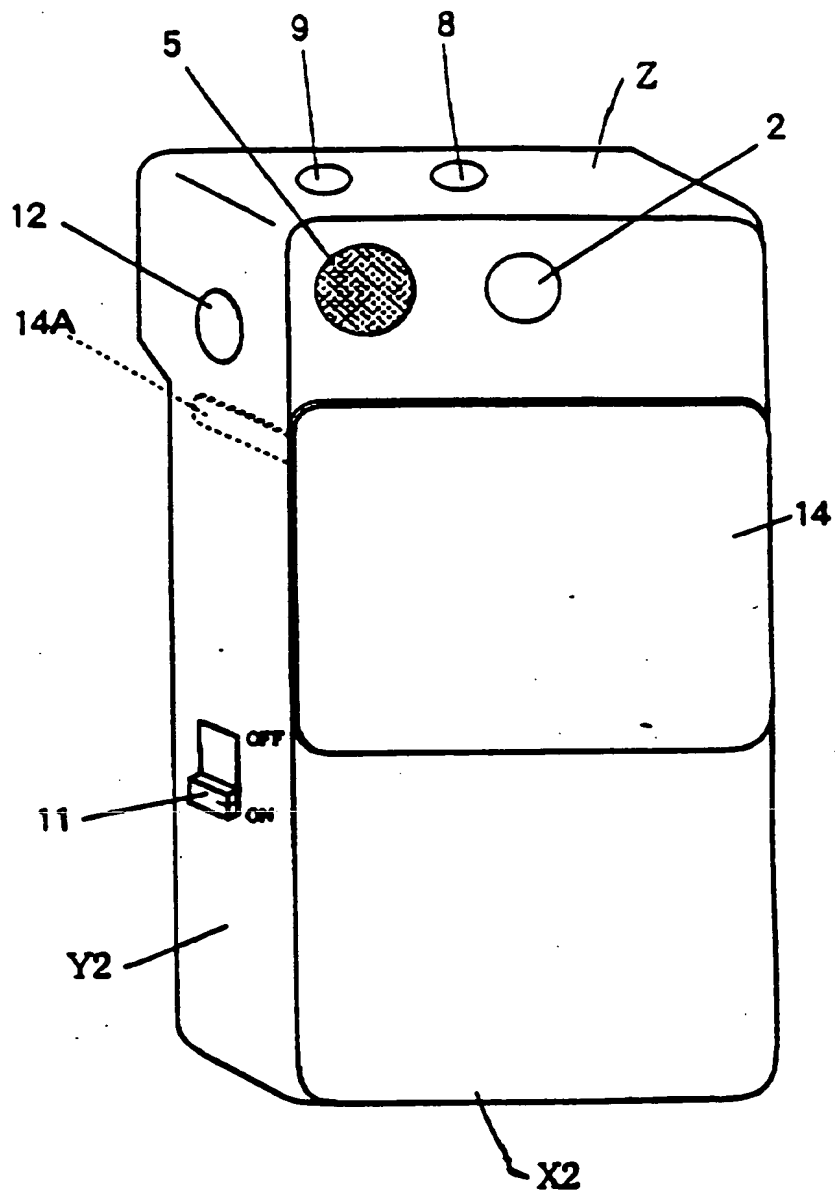


FIG. 3

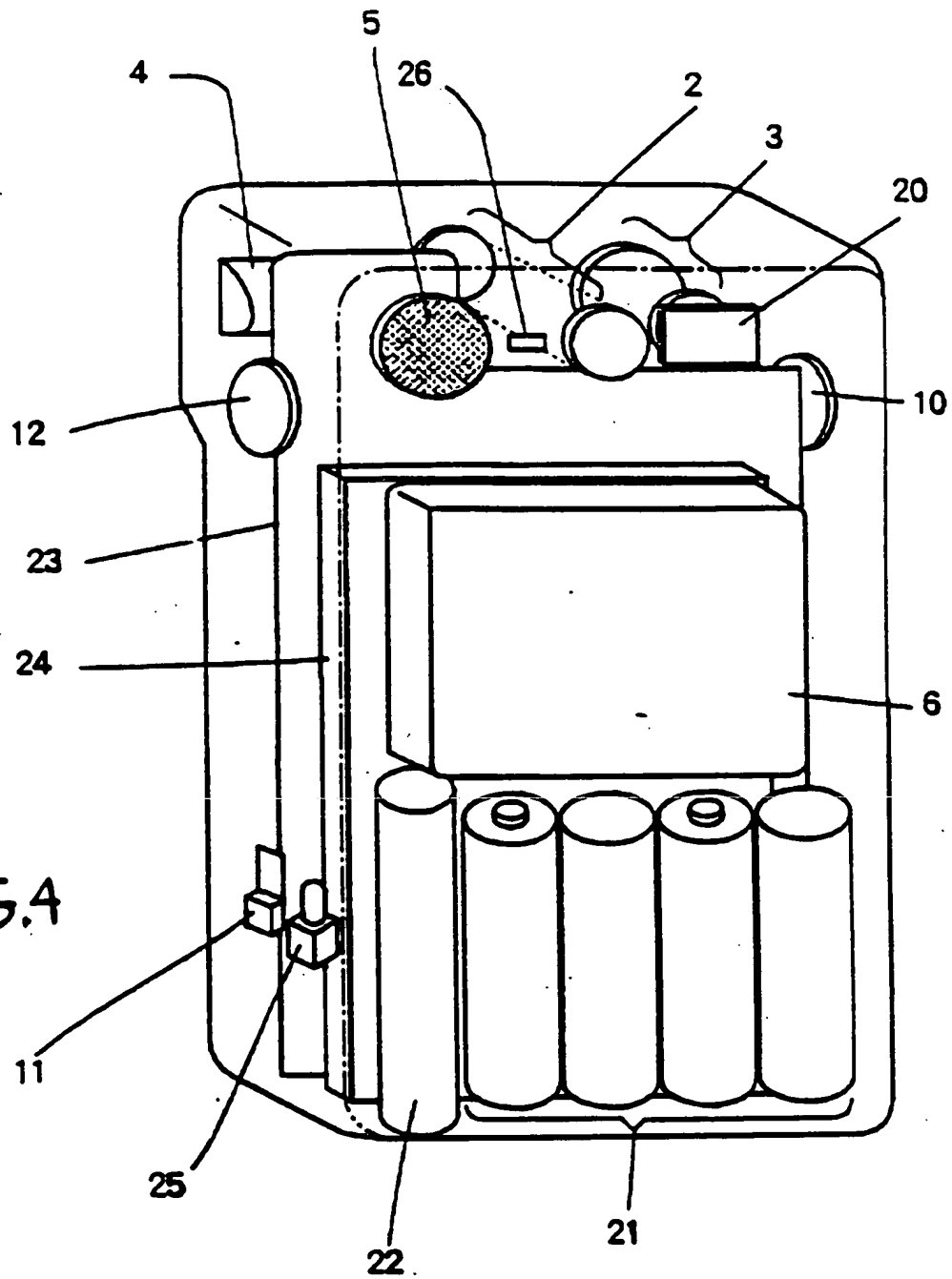


FIG. 5(A)

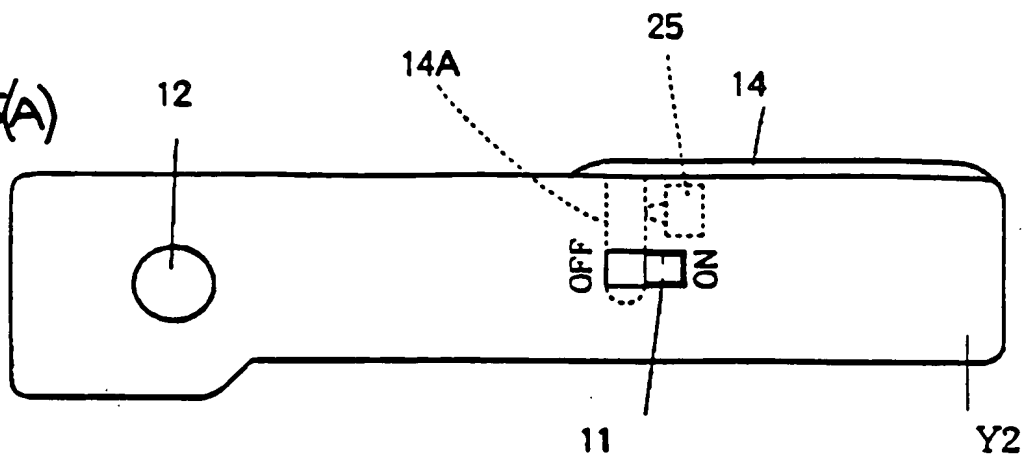


FIG. 5(B)

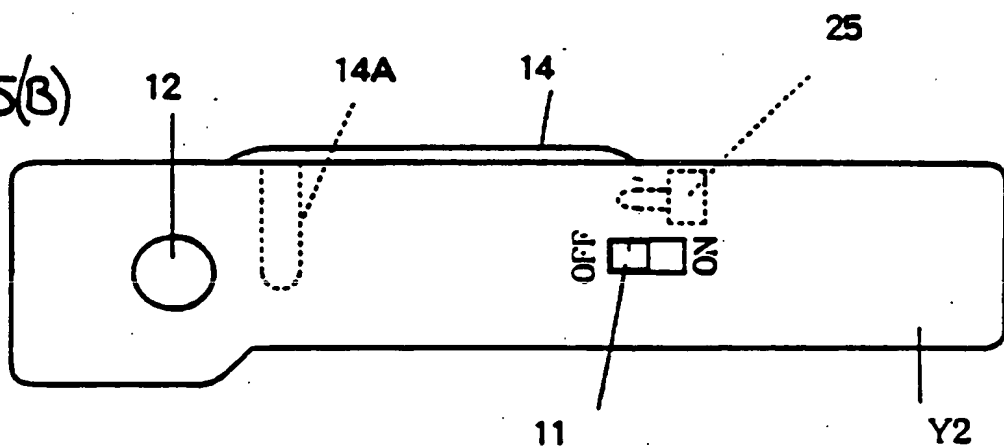


FIG. 5(C)

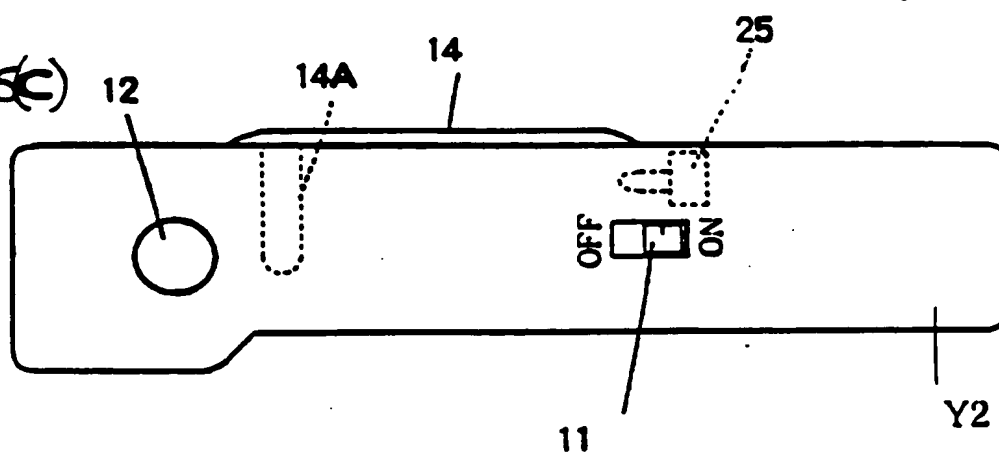
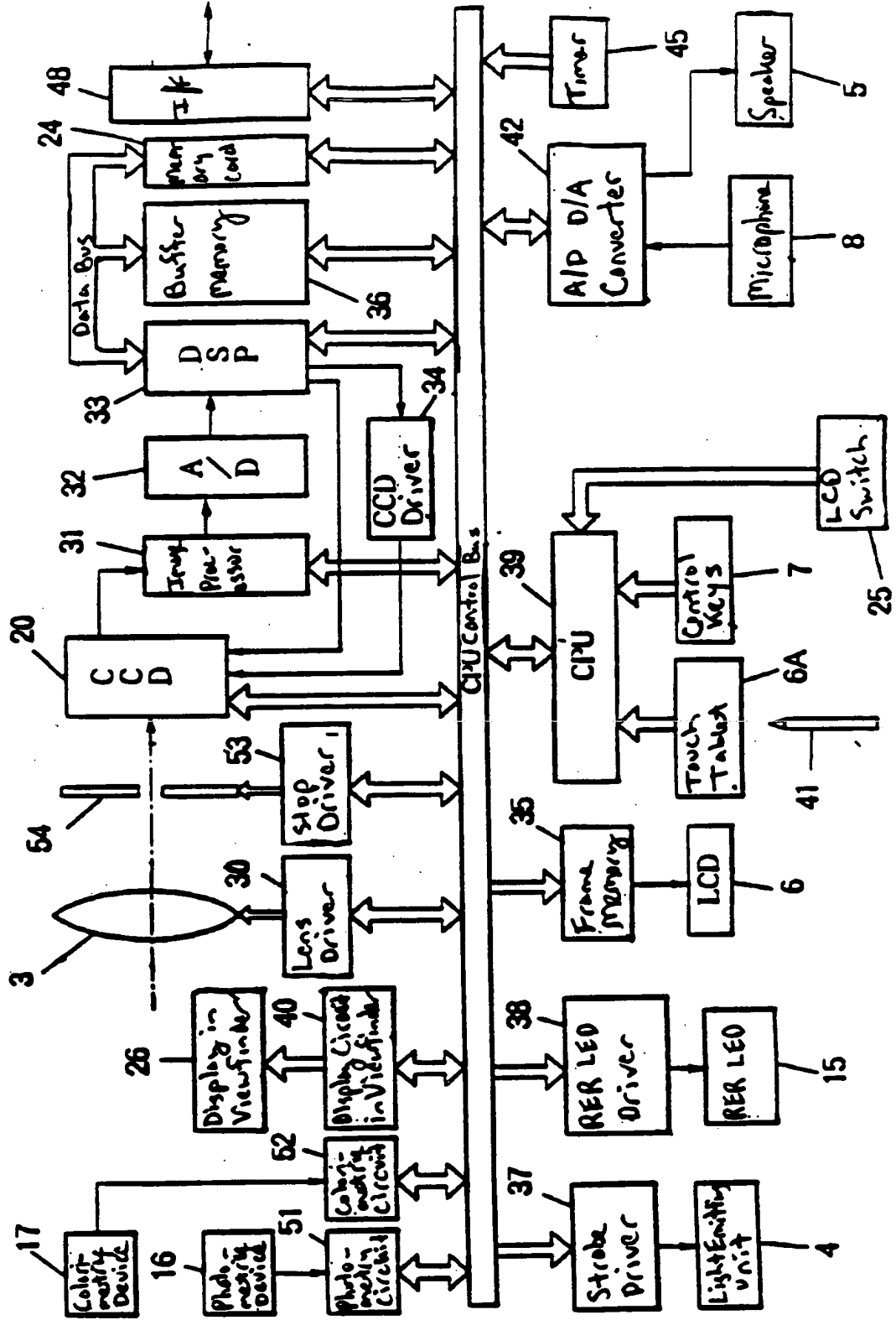


FIG. 6



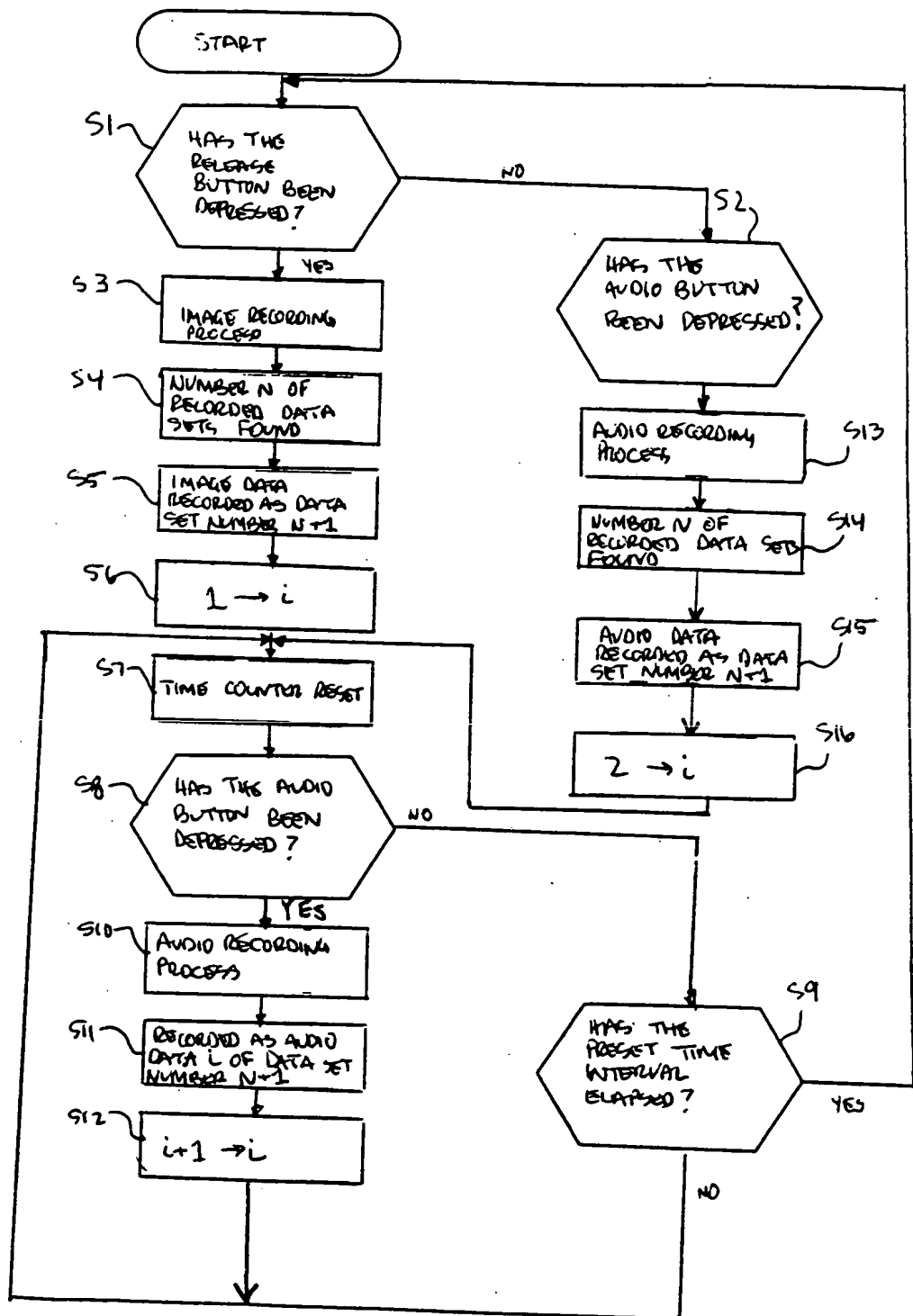
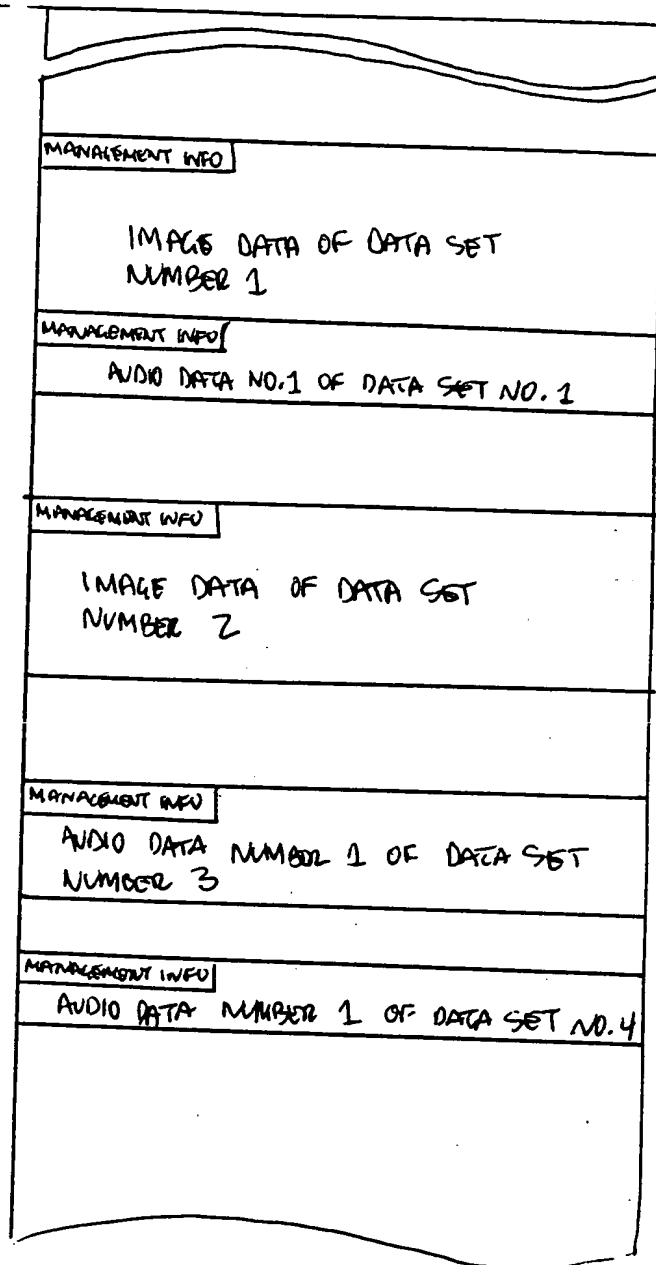


FIGURE 7

ADDRESS
#0000



MEMORY
SPACE

FIGURE 8

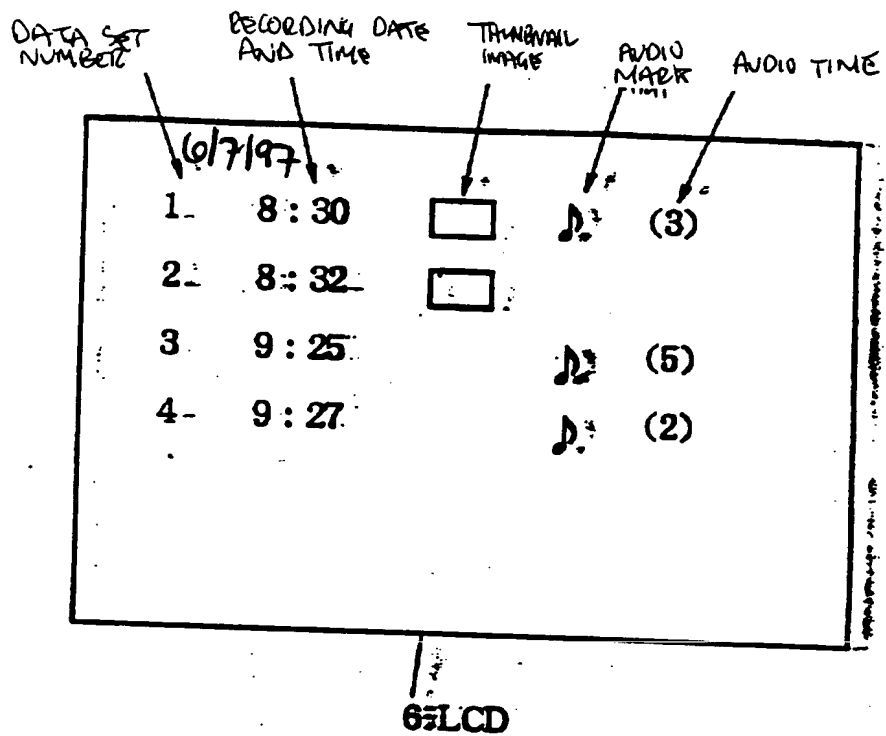


Figure 9

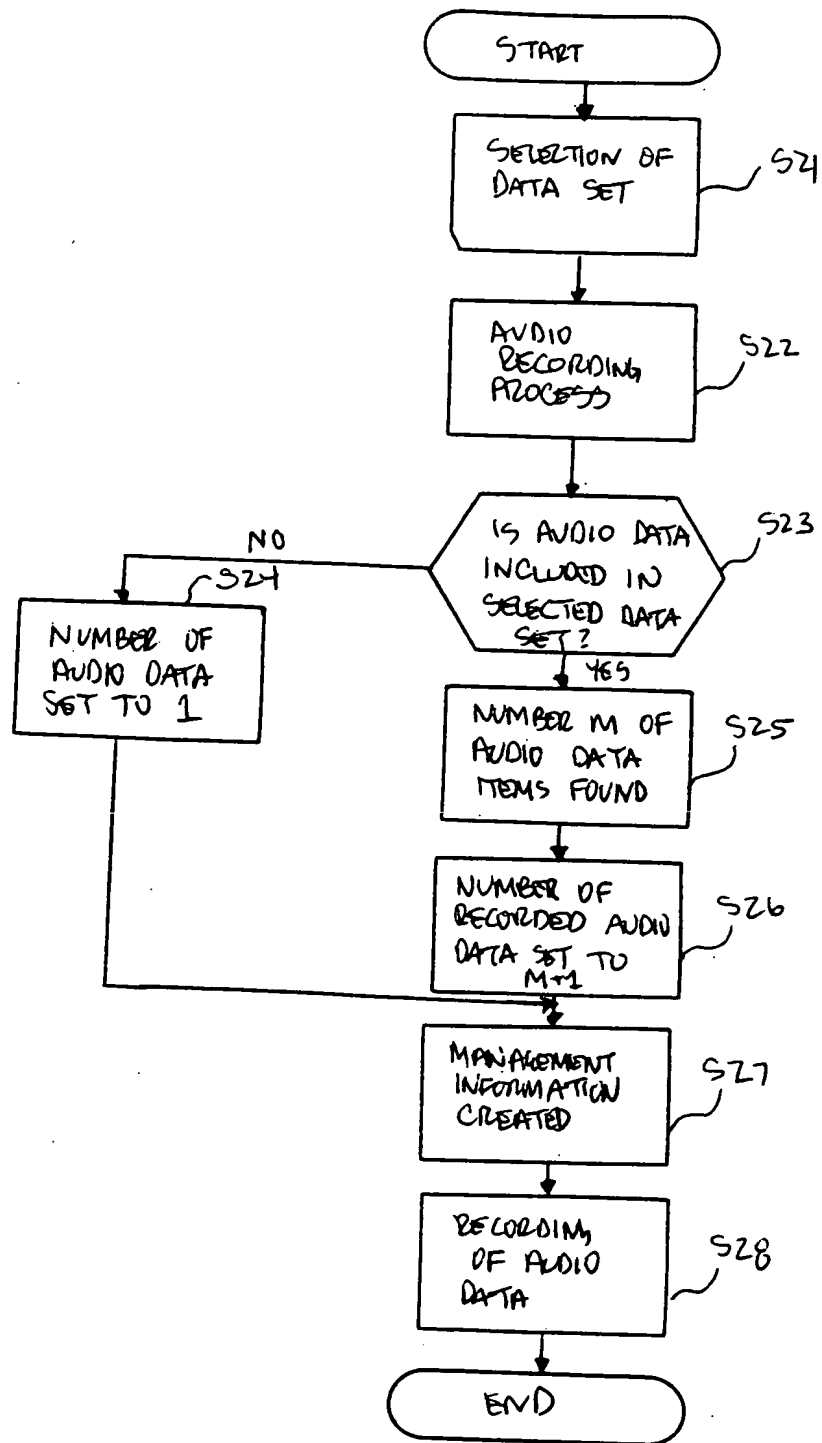


FIGURE 10

ADDRESS ---
0000

MEMORY
SPACE

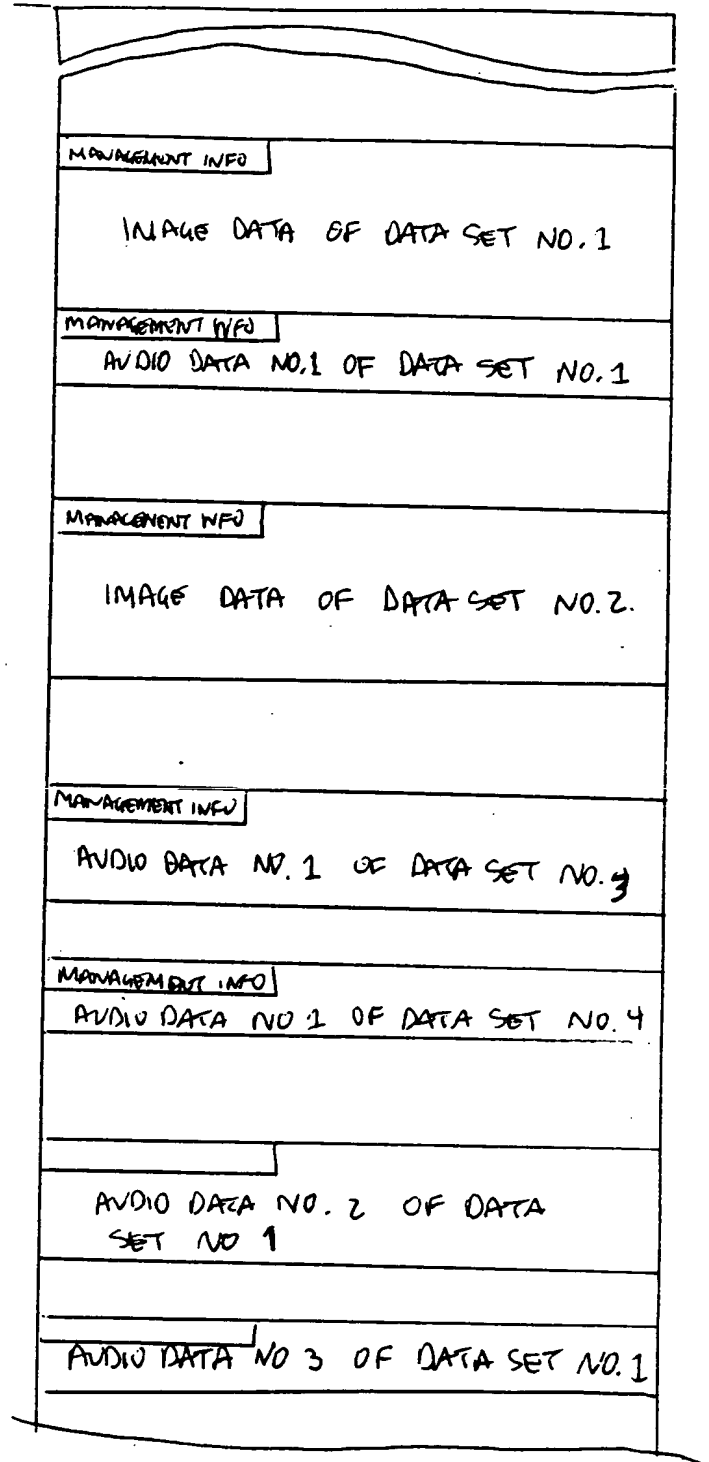


FIGURE 1)

FIGURE 12 (A)

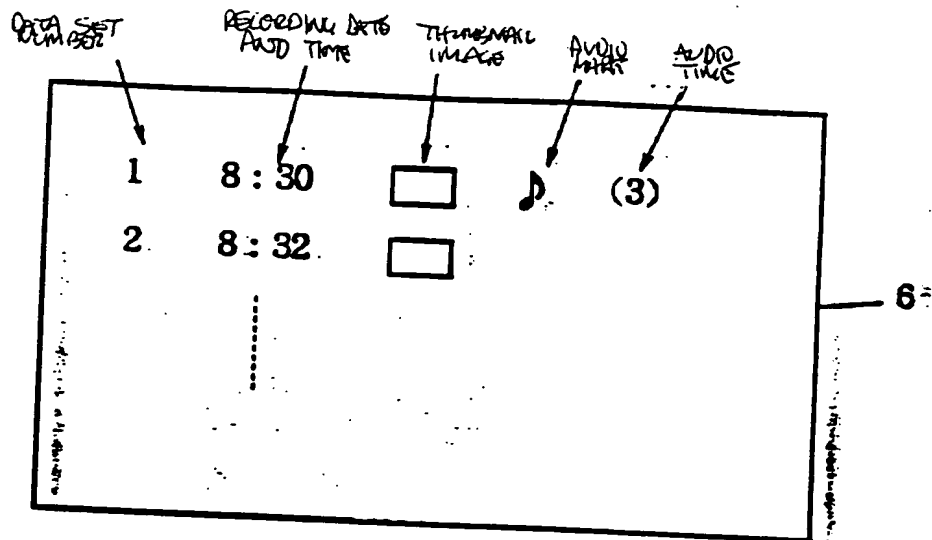


FIGURE 12 (B)

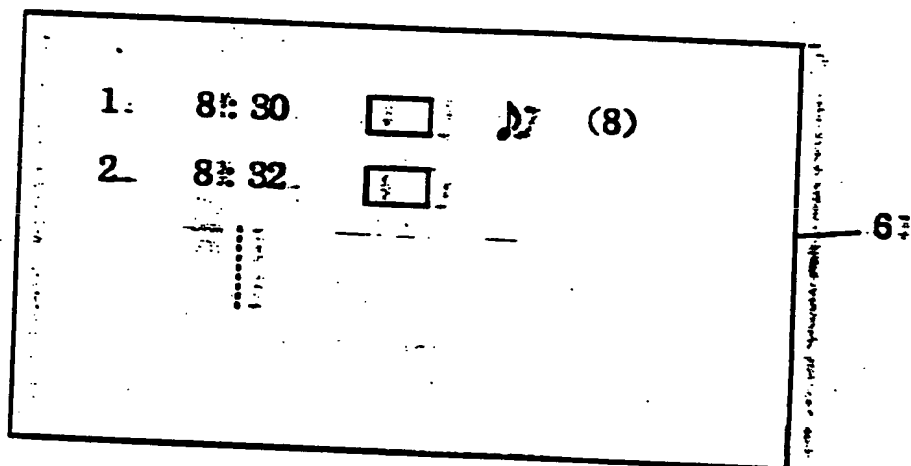


FIGURE 12 (C)

